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PATENT NUMBER and ISSUE DATE

U.S. UTILITY Patent Application

1	APPL N	JM F	ILING DATE	CLASS	SUBCLASS	GAU			
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	**APPLIC	ANTS:	Surve S	watee;					
			ATA VERIFIED						
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<u></u>	TITLE: Deposition of electronic circuits on fibers and other materials U.S.DEPT. OF COMM./PAT. & TM-PTO-436L(Rev. 12-94)								
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		Assistant Examiner	Total Claims	P	rint Claim for .G		
	UE FEE		DRAWING				
Amount Due	Date Paid		Sheets Drwg.	Figs.Drwg.	Print Fig.		
		Primary Examiner					
TER	MINAL	PREPARED FOR ISSUE	Application Examiner				
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